

UNITED STATES PATENT APPLICATION

For

**APPARATUS AND METHOD FOR PREVENTING
FORGERY/ALTERATION OF THE DATA RECORDED BY DIGITAL
VOICE RECORDER**

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Attorney Docket No.: 006331.P008

"Express Mail" mailing label number: EV 336589397 US
Date of Deposit: October 14, 2003
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APPARATUS AND METHOD FOR PREVENTING FORGERY/ALTERATION OF THE DATA RECORDED BY DIGITAL VOICE RECORDER

5 CROSS-REFERENCE TO RELATED APPLICATIONS

The present patent application claims priority from Republic of Korea Patent Application No. 2002-62768, filed on October 15, 2002.

FIELD OF THE INVENTION

10 The present invention relates to an apparatus and method for preventing forgery/alteration of the data recorded by digital voice recorder (DVR). More particularly, it relates to an apparatus and method preventing forgery/alteration of recorded data by going through a predetermined process using the forgery or alteration prevention unit on data recorded by a digital voice recorder.

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DESCRIPTION OF THE RELATED ART

Digital voice recorder is an apparatus recording data on a recording medium such as flash memory, DAT, audio tape by transforming voice, sound, and other noises into digital signals.

20

Figure 1 is a block diagram illustrating the constitution of a conventional digital voice recorder.

Referring to Figure 1, a digital voice recorder 100 comprises a microphone 101, input signal controller 102, A/D converter 103, CPU or micro-controller 104, D/A

converter 105, output signal controller 106, speaker or headphone 107, data compressor/de-compressor 110, data storage unit 111 and data transmitter 109, and it can be connected to the Personal Computer (PC) 108 by the data transmitter 109 in accordance with users need.

5 An input audio signal from the microphone 101 is controlled to fit in the movement range of the A/D converter 103 at the input signal controller. The input analog audio signal is converted into digital audio signal at the A/D converter 103. The CPU or micro-controller 104 controls all operation of the DVR. The micro-controller 104 is a chip running a program for performing the operation
10 of the DVR, independently, by building a program ROM, data RAM, CPU inside it.

 Data compressor/de-compressor 110 stores digital audio data at the data storage unit 111 after compressing digital audio data or decompresses the compressed digital data at the data storage unit 111. Output signal controller 106 controls the range of the signal so that the analog audio signal converted by the
15 D/A converter 105 fits the input of the speaker or headphone. The speaker or headphone 107 outputs the stored audio signal outside the digital voice recorder.

 Meanwhile, data transmitter 109 is used to transmit the data recorded by the digital voice recorder to the PC. The data transmitter 109 is an apparatus implementing a communication means connecting the PC comprising serial
20 communication (RS-232, UBS, IEEE1394, etc.), parallel communication (PC parallel port communication: SPP, ECP, EPP, etc.), etc. to the digital voice recorder. The PC 108 receives the data recorded by the digital voice recorder 100. The

connection of the power supply 112 is not shown in the figures in detail, but the power supply 112 supplies power to the above devices, which can be implemented by battery or AC-DC converter.

A brief description of the operation of the above digital voice recorder 100 is as follows; first, outer audio signal is converted into electronic signal by the microphone 101, and controlled to fit in the input range of the A/D converter 103 at the input signal controller 102. The controlled audio signal is converted into digital audio signal by the A/D converter 103. In order to record audio signal, the converted digital audio signal is compressed by the data compressor/de-compressor and then stored at the data storage unit 111 by the CPU or micro-controller 104.

In order to play the audio signal recorded by the digital voice recorder 100, the audio signal stored at the data storage unit 111 is decompressed at the data compressor/de-compressor 110, and then converted into analog signal at the D/A converter 105. The output signal controller 106 converts the converted analog audio signal into a signal with proper range so that it can be output through a speaker or headphone.

Also, in order to transmit the audio signal to the PC 108, as in the steps for playing the audio signal, it is decompressed at the data storage unit 111 by a data compressor/de-compressor 110, and then transmitted to the PC 108 by a data transmitter 109.

The above-described digital voice recorder is mainly used when it is required

to record a conversation. For example, it can be used to record the contents of a conference, which may lead to a dispute later, such as a client's order information on selling or buying stock at a stock company.

5 However, because there is distrust on the message recorded by the digital voice recorder, which is raised by the distrust between the party possessing the digital voice recorder and the opposite party, and because the recorded audio data can be forged or altered without trouble, a security solution which can prevent forgery/alteration of such recorded audio data is strongly needed.

10 In this regard, there were no countermeasures to prevent forgery/alteration of audio data recorded by digital voice recorder in the past. Especially, since the authority for controlling the digital voice recorder is concentrated on one side of the party, there is a high possibility to dispute its authentication with the other party.

SUMMARY OF THE INVENTION

In order to overcome the above problems, the present invention provides an apparatus and method for preventing forgery/alteration of the data recorded by the digital voice recorder, i.e., a watermark embedding unit, encryption unit or hash value inserting unit which can authenticate forgery/alteration of the data later.

Accordingly, the present invention provides an apparatus and method for authenticating whether the recorded data has been forged/alterated when the data is recorded by a digital voice recorder or transmitted to the PC after being recorded at a digital voice recorder, by using a technology which can prevent or authenticate forgery/alteration of data in real-time when data is recorded by the digital voice recorder or when data is transmitted to the PC from the digital voice recorder.

In order to accomplish the above object, the present invention provides a forgery or alteration prevention apparatus for inserting an information for preventing forgery or alteration of an audio data into the audio data which is stored in DVR (Digital Voice Recorder), said DVR comprises an audio data input unit, A/D converter converting an analog audio data from said audio data input unit into a digital audio data, and a data storage unit storing said digital audio data, wherein said forgery or alteration prevention apparatus receives said digital audio data from said A/D converter, and inserts said information for preventing forgery or alteration into said digital audio data before storing said digital audio data in said data storage unit.

Preferably, said apparatus is implemented within the digital voice recorder in

the form of general PCB board, DSP chip board, FPGA (Flexible Program Gate Array) board, ASIC (Application Specific Integrated Circuit) board, or software programs.

Further, in a system for receiving a digital audio data stored in DVR and
5 storing said digital audio data in PC, a forgery or alteration prevention apparatus
for inserting an information for preventing forgery or alteration of said digital
audio data, wherein said forgery or alteration prevention apparatus is provided in
said PC, and inserts said information for preventing forgery or alteration into said
digital audio data before storing said digital audio data in a data storage unit in
10 said PC.

In accordance with the first embodiment of the invention, the insertion of said
information for preventing forgery or alteration is carried out by embedding
watermark into said digital audio data, and the confirmation of whether said stored
digital audio data has been forged or altered is carried out by detecting said
15 watermark.

Preferably, said watermark is one of robust watermark or semi-fragile
watermark, and the embedment of said watermark is carried out before the
compression of said digital audio data.

Preferably, said watermark is fragile watermark; and the embedment of said
20 watermark is carried out after the compression of said digital audio data.

In accordance with the second embodiment of the invention, the insertion of
said information for preventing forgery or alteration is carried out by encrypting

said digital audio data by predetermined encryption key.

In accordance with the first embodiment of the invention, the insertion of said information for preventing forgery or alteration is carried out by inserting hash value of said digital audio data into said digital audio data, and the confirmation of whether said stored digital audio data has been forged or altered is carried out by confirming whether the hash value newly obtained by applying said stored digital audio data to a hash function used for obtaining said hash value is identical to the hash value inserted in said stored digital audio data.

In order to accomplish the other object of the present invention, the present invention provides a method for inserting an information for preventing forgery or alteration of an audio data stored in DVR, said DVR comprises an audio data input unit, A/D converter converting an analog audio data from said audio data input unit into a digital audio data, and a data storage unit storing said digital audio data, the method comprising: (a) receiving said digital audio data from A/D converter; (b) inserting said information for preventing forgery or alteration into said digital audio data in real time; and (c) storing said digital audio data into which said information for preventing forgery or alteration is inserted in said data storage unit.

Meanwhile, the present invention provides a method for inserting an information for preventing forgery or alteration into said received digital audio data in said PC in a system for receiving a digital audio data stored in DVR and storing said digital audio data in PC, the method comprising: (a) receiving said digital audio data stored in said DVR; (b) inserting said information for preventing

forgery or alteration into said digital audio data in real time; and (c) storing said digital audio data into which said information for preventing forgery or alteration is inserted in said PC.

In accordance with the first embodiment of the present invention, said step (b)
5 is carried out by embedding watermark into said digital audio data, and the confirmation of whether said stored digital audio data has been forged or altered is carried out by detecting said watermark.

In accordance with the second embodiment of the present invention, said step (b) is carried out by encrypting said digital audio data with a predetermined
10 encryption key.

In accordance with the third embodiment of the present invention, said step (b) is carried out by inserting hash value of said digital audio data into said digital audio data, and the confirmation of whether said stored digital audio data has been forged or altered is carried out by confirming whether hash value inserted in said
15 digital audio data is identical to the newly obtained hash value from applying said stored digital audio data to the hash function used for obtaining said hash value.

BRIEF DESCRIPTION OF THE DRAWINGS

Figure 1 is a block diagram illustrating the constitution of a conventional digital voice recorder;

Figure 2 is a block diagram illustrating the constitution of a digital voice recorder wherein the forgery/alteration prevention unit in accordance with the present invention is installed outside the recorder;

Figure 3 is a block diagram illustrating the constitution of a digital voice recorder wherein the forgery/alteration prevention unit in accordance with the present invention is installed inside the recorder;

Figure 4a is a drawing illustrating the embedding and detecting process of the watermark in accordance with the first embodiment of the present invention;

Figure 4b is a block diagram illustrating the constitution of the watermark-embedding unit in accordance with the first embodiment of the present invention;

Figure 5a is a drawing illustrating the encryption and decryption process in accordance with the second embodiment of the present invention;

Figure 5b is a block diagram illustrating the constitution of the encryption unit in accordance with the second embodiment of the present invention;

Figure 6a is a drawing illustrating the data authenticating process using hash function in accordance with the third embodiment of the present invention; and

Figure 6b is a drawing illustrating the constitution of the hash value-inserting unit in accordance with the third embodiment of the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Hereinbelow, the preferred embodiments of the present invention are described in more detail in the following with reference to the accompanied drawings.

5 First, the constitution of the forgery/alteration prevention unit in accordance with the present invention (more particularly, the forgery/alteration prevention unit may be a watermark embedding unit, an encryption unit, or a hash value inserting unit, which will be described in more detail in the following with reference to Figs. 4 to 6) wherein various embodiments of the digital voice recorder
10 are applied will be described with reference to Figure 2 and Figure 3.

Figure 2 is a block diagram illustrating the constitution of a digital voice recorder wherein the forgery/alteration prevention unit in accordance with the present invention is installed outside DVR.

Referring to Figure 2, the forgery or alteration prevention unit 203 is installed
15 inside the PC 205, and a forgery/alteration prevention means is inserted into the data received from the digital voice recorder 202. As for a more detailed description on the operation of the forgery or alteration prevention unit 203, first an audio signal is input from the microphone 201. The input audio signal is converted into digital and recorded by the digital voice recorder 202. Then, said
20 audio signal is stored permanently or uploaded to the PC 205 by the data transmitter 109 if needed. At this time, a predetermined forgery or alteration prevention means is inserted by the forgery or alteration prevention unit 203

installed in the PC 205. The forgery or alteration prevention unit 203 is the unit wherein inserts information for authentication into the data to be recorded and the inserted information can be used when determining whether the recorded data is forged/altered later. Then, the audio data is stored in a predetermined storage
5 medium (data storage unit 204) such as a hard disc drive inside the PC 205.

Said forgery or alteration prevention unit 203 may be a watermark embedding unit, an encryption unit, or a hash value inserting unit, and description thereof will be described in more detail in the following with reference to Figs. 4 to 6.

Figure 3 is a block diagram illustrating the constitution of a digital voice
10 recorder wherein the forgery/alteration prevention unit in accordance with the present invention is installed inside the recorder.

Referring to Figure 3, information for preventing forgery/alteration is inserted into said audio signal at the forgery or alteration prevention unit 302 before the audio signal received from the microphone 301 is converted into and stored in the
15 data storage unit 204 of the digital voice recorder 302. Then, the audio signal is transmitted to the PC 303, and may be stored in the PC 303 as well.

In Figure 3, the forgery or alteration prevention apparatus 3022 installed inside the digital voice recorder according to the present invention is implemented to carry out its operation at CPU or micro-controller 104, but a person skilled in the art
20 may also embody the forgery or alteration prevention apparatus 3022 as a separate individual device.

The forgery or alteration prevention apparatus 3022 installed inside the digital

voice recorder may be a watermark embedding unit, an encryption unit, or a hash value inserting unit, and the description thereof will be made in reference to Figs. 4 to 6 hereinafter.

The forgery or alteration prevention apparatus 203, 3022 of Figs. 2 & 3 can be
5 implemented in the form of an general PCB board, a DSP chip board, an FPGA (Flexible Program Gate Array) board, an ASIC (Application Specific Integrated Circuit) board, or software programs.

The specific forgery or alteration prevention technology and the internal constitution of forgery or alteration prevention apparatus 203, 3022 according to the
10 first, second and third preferred embodiments of the present invention used in the aforementioned forgery or alteration prevention apparatus 203, 3022 are described hereinafter.

The first embodiment of the present invention is the embodiment of the forgery or alteration prevention apparatus according to the method of
15 watermarking.

Figure 4a is a drawing illustrating embedment and detection process of the watermark according to the first embodiment of the present invention.

Referring to Figure 4a, the watermark-embedding unit 401 embeds watermark into the input audio signals. The embedded watermark information is the
20 information verifying the authenticity of the audio data later. The watermark-embedding unit 401 refers to the aforementioned forgery or alteration prevention apparatus of the present invention. Hereinafter, the audio data inserted with

watermark is stored in the storage unit, thereby the process of inserting information for preventing forgery or alteration according to the present invention is completed.

The types of watermark usable in the present embodiments include Robust Watermark (RW), Semi-Fragile Watermark (SFW), and Fragile Watermark (FW),
5 and watermark is also classified according to the extent of how much the watermark endures against the external attack or alteration.

The robust watermark refers to watermark capable of extracting watermark information even at the attack such as compression and A/D conversion, noise addition, etc. The semi-fragile watermark is characterized in that the watermark
10 remains intact against the attack such as compression, but the watermark will break down against the malicious attack such as alteration of the audio data. The fragile watermark is characterized in that the watermark will break down even at the slight alteration. Thus, if using a fragile watermark in the present invention, watermark should be embedded after the compression of the audio data when compressing
15 and storing data.

Although Figure 4a illustrates as a continuous process, it is general for the audio data embedded with watermark in the forgery or alteration prevention apparatus according to the present invention when it becomes a matter of concern whether the data has been forged or altered that the process of extracting
20 watermark is carried out from the separate off-line watermark extracting unit (may be a software program embodied in PC). The extraction of the watermark may be certification of the information embedded as watermark itself, and also it is possible

to check whether audio data stored has been forged or altered with respect to whether the watermark has been broken down.

Figure 4b is a block diagram illustrating the constitution of the watermark-embedding unit according to the first embodiment of the present invention. That is, the forgery or alteration prevention apparatus of Figs. 2 & 3 is implemented as a watermark-embedding unit according to the first embodiment of the present invention.

Referring to Figure 4b, the watermark-embedding unit 401 according to the first embodiment of the present invention comprises an input signal controller 412, an analog-digital converter 413, a digital signal processor or a micro-controller 414, a digital-analog converter 415, an output signal controller 416, and a power supplying unit 418.

The input signal controller 412 adjusts the analog input signals 401 (audio signals) input from the aforementioned microphone to satisfy the input range of the A/D converter 413. The adjusted analog audio signals are converted to digital audio signals by the A/D converter 413.

The converted audio signals are inserted with watermark at the digital signal processor or micro-controller 414, and the audio signals embedded with watermark are converted again to analog signals (i.e., audio signals) at the D/A converter 415, then the analog signals embedded with watermark is adjusted into a predetermined range at the output signal controller 416 so as to be output analog output signals 417. Meanwhile, the power supply 418 supplies necessary power to a digital

signal process or micro-controller 414.

Meanwhile, if the desired watermark is the robust watermark or semi-fragile watermark, it is desirable to carry out embedment of watermark before the converted digital audio data is compressed. Generally, if the desired watermark is the fragile watermark, it is desirable to carry out the embedment of watermark after
5 the converted digital audio data is compressed.

The second embodiment of the present invention is the implementation of the forgery or alteration prevention apparatus according to the use of an encryption algorithm.

10 Figure 5a is a drawing illustrating an encryption and decryption process in accordance with the second embodiment of the present invention.

Referring to Figure 5a, the encryption unit 501 encrypts the inputted audio data by a predetermined encryption algorithm. In this regard, a predetermined encryption key is used in the encryption. The encrypted data is stored in the data
15 storage unit (not shown).

If the audio data is encrypted by the predetermined encryption key at the forgery or alteration prevention apparatus according to the present invention, it is possible to confirm whether the stored audio data has been forged or altered by carrying out the decryption process to the encrypted data at the separate off-line
20 decryption unit (it may be a software program embodied in PC) when the alteration or forgery of the later data becomes a matter of concern. The types of the encryption system according the preferred embodiment of the present invention

include symmetric cryptosystem (may also be called 'public key encryption system') and asymmetric cryptosystem (may also be called 'secret key encryption system'). In the symmetric encryption system, the encryption key is the same as the decryption key, but in the asymmetric encryption system, the encryption key is
5 different from the decryption key.

The symmetric encryption algorithm is characterized in that the speed of encryption and decryption is fast, which includes DES, 3DES, SEED and Rijndael, etc. The asymmetric encryption algorithm is characterized in that it is difficult to solve mathematically and it takes long to decrypt, which includes RSA, ElGamal,
10 and ECC, etc. It does not matter for the present invention to use any of the encryption algorithm.

The preferred embodiment of the present invention uses Rijndael algorithm of 128 bits adopted as AES (Advanced Encryption Standard) by NIST of U.S. and conducted the experiment, and the result of the experiment is as follows. Such
15 result of the experiment refers to resource required to make Raw Data decrypted into 256 bytes from the data encrypted into 256 bytes when implementing Rijndael algorithm by using TI DSP (TI Digital Signal Processor) of U.S.

- * Resource used when decrypting at TI DSP (TMS 320VC5410): 256 bytes
- * Capacity of operation: approximately 20 MIPS (Million Instruction Per
20 Second)

- * Size of the program code memory used for decryption at TI DSP: 10 KB
(10,714 bytes)

* Size of the data memory used for decryption at TI DSP: 1KB (1,024 bytes)

Figure 5b is a block diagram illustrating the constitution of the encrypting unit in accordance with the second embodiment of the present invention. Unlike Figure 4b, the digital signal processor or micro-controller 513 does not carry out the function of embedding watermark, but carries out the function of encrypting digital audio data by using encryption key. Otherwise, the same description made with regard to Figure 4b is applied.

The third embodiment of the present invention is implementation of the forgery or alteration prevention apparatus using a hash function.

Figure 6a is a drawing illustrating the data authentication process using hash function in accordance with the third embodiment of the present invention.

Referring to Figure 6a, the inputted audio data is input in the hash function, which can be indicated as Equation 1 as below. It is desirable to use MD5 (Message Digest 5) as the usable hash function, however, it is not limited thereto.

[Equation 1]

$$M_1 = H(I)$$

Herein, I is the input data, H(x) is the hash function, and M₁ is the calculated hash value.

The hash value M₁ which is the output value of the hash function is inserted in the input audio data, and the audio data is stored in the storage medium in a state wherein the hash value M₁ is inserted.

The characteristic of the hash function is that it is a one-way function. That is,

it is impossible to infer the input data inputted in the hash function only with the hash value.

When the audio data inserted with the hash value calculated by a predetermined hash function at the forgery or alteration prevention apparatus according to the present invention has a problem of whether the data is forged or altered later, the verification process of the hash value is carried out at the separate off-line hash value verification unit (it may be a software program implemented in PC) and thus it is possible to confirm whether the stored audio data is forged or altered.

10 The specific process of verifying a hash value is as follows. The stored audio data is comprised of the input audio data itself and the calculated hash value M_1 by applying the hash function to the data. A new hash value M_2 is obtained by using a hash function identical to that used for input data at the hash value-inserting unit (that is, the forgery or alteration prevention apparatus according to the present invention). If the hash value M_1 inserted into the stored audio data is identical to 15 the newly obtained hash value M_2 , the stored audio data is the original data which has not been forged or altered. If the two hash values M_1 and M_2 are different from each other, the stored audio data is considered to be forged or altered.

Figure 6b is a drawing illustrating the constitution of the hash value-inserting unit in accordance with the third embodiment of the present invention. Unlike 20 Figure 4b, the digital signal processor or micro-controller 513 does not carry out the function of embedding watermark, but calculates the hash value with regard to the

digital audio data and carries out the function of inserting the calculated hash value into the specific portion of the audio data. Otherwise, the same description made with regard to Figure 4b is applied.

The aforementioned present invention provides an effect of confirming
5 whether the stored audio data is forged or altered when it is necessary to confirm the stored audio data by using a technology preventing or confirming the forgery or alteration of the data in real-time when recording conversation data on the digital voice recorder (DVR).

The present invention has been described specially by illustration with
10 reference to the above embodiments, but such description is made for exemplification, and a person having ordinary skill in the art to which the present invention pertains is aware that he may variously change without deviating from the technical idea and scope of the present invention as defined in the accompanied claims.